TRIBHUVAN UNIVERSITY

INSTITUTE OF ENGINEERING
IOE CENTRAL CAMPUS, PULCHOWK

Major Project Proposal

Title

Submitted by:
Name (Rank)
Name (Rank)
Name (Rank)
Name (Rank)

Submitted to:
Department of Electronics and Computer Engineering

December 30, 2016

FPGA BASED IMPLEMENTATION OF DIGITAL IMAGE PROCESSING TECHNIQUES

MINOR PROJECT REPORT

Name (Rank)
Name (Rank)
Name (Rank)
Name (Rank)

SUPREVISOR
Dr ...

"MAJOR PROJECT REPORT SUBMITTED FOR PARTIAL FULLFILLMENT OF THE DEGREE OF BACHELORS' IN ELECTRONICS AND COMMUNICATION ENGINEERING"

DEPARTMENT OF ELECTRONICS AND COMPUTER ENGINEERING

IOE CENTRAL CAMPUS
PULCHOWK, LALITPUR

December 30, 2016

Certificate of approval

The undersigned certify that the final year project entitled **"Title"** submitted by **Members** to the Department of Electronics , Communication and Computer Engineering in partial fulfillment of requirement for the degree of Bachelor of Engineering in Electronics and Computer Engineering. The project was carried out under special supervision and within the time frame prescribed by the syllabus. We found the students to be hardworking, skilled, bonafide and ready to undertake any commercial and industrial work related to their field of study and hence we recommend the award of Bachelor of Electronics and Communication Engineering degree.

We found the students to be hardworking, skilled, bonafide and ready to undertake any commercial and industrial work related to their field of study and hence we recommend the award of Bachelor of Computer Engineering degree.

1. .....
Dr
(Project Supervisor)

2. .....
Name:.....
(External Examiner)

3. .....
Dr
Head of Department

Copyright

The author has agreed that the library, IOE Central Campus Pulchowk may make this report freely available for the inspection. Moreover, the author has agreed the permission for the extensive copying of this project report for the scholarly purpose may be granted by supervisor who supervised the project work recorded herein or, in his absence the Head of The Department wherein the project was done. It is understood that the recognition will be given to the author of the report and to the Department of Electronics and Computer Engineering, IOE Central Campus Pulchowk in any use of the material of this project report. Copying or publication or other use of this report for financial gain without approval of the department and author's written permission is prohibited. Request for this permission to copy or to make any other use of material in this report in whole or in part should be addressed to:

Head of Department
Department of Electronics and Computer Engineering
IOE Central Campus Pulchowk
Pulchowk,
Lalitpur, Nepal

Acknowledgment

We would like to express our heartfelt gratitude to Department of Electronics and Computer Engineering of Institute of Engineering, Pulchowk Campus for constructing arena for exchange of knowledge and personal creativity development. All the mentors and the resources provided by the College has been crucial in our vision that we present today. Projects are at the heart of engineering and development of the society. This projects will build necessary maturity to handle real world projects in the future.

We would like to extend our gratitude to **Dr ..**, **Dr ...** and **...** for providing us with all the necessary guidance and help to come in this stage.

Abstract

Contents

Certificate of approval i

Copyright ii

Acknowledgment iii

Abstract iv

Table of contents v

List of tables vi

List of Figures vii

List of Symbols and Acronyms viii

1 Introduction 1

Appendix 2

References 3

List of Tables

List of Figures

1 Introduction

Appendix

References

[1] Sujit Maharjan. example. *IOE, TU*, 1, 4.